

Posted by [Kim](#) on Fri, 16 Dec 2005 12:56:58 GMT

3Wa4yca2tr3t7ezs7OxsbG tbW1nJycnJyclJSU
IJSUIJSUAAAAIJSUIJSUIJSUIJSUIJSUIJSUIJSUY2Njra2txu/35+/3//// vd7vpabee5StUnOM
UmtzMTExWlpaWlpaWlpaWlpaWlpaWlpaWlpaWlpaWlpaWlpa//8A//8A //8AWlpaWlpaWlpa
WlpaWlpaWlpaWlpa//8A//8A//8AWlpaWlpaWlpaWlpa//8A//8A//8A//8A WlpaWlpaWlpa//8A
//8A//8AWlpaWlpaWlpaWlpaWlpaWlpaWlpaWlpa//8A//8A//8A//8AWlpaWlpa WlpaWlpaWlpaWlpa
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tdbntdbnrc7erc7erc7evd7vvd7v5+/35+/35+/3////5+/3vd

Posted by [Miguel Vigil \[1\]](#) on Fri, 16 Dec 2005 14:46:22 GMT

[illegible]

Posted by [Aaron Allen](#) on Fri, 16 Dec 2005 15:09:19 GMT

bntdbntdbntdbntdbntdbntdbn tdbntdbntdbntdbn
tdbntdbntdbntdbntdbntdbntdbntdbntdbntdbntdbntdbntdbn tdbntdbntdbntdbn
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Subject: Re: Paris Analog Input Calibration Technique
Posted by [duncan](#) on Mon, 19 Dec 2005 16:44:28 GMT
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>>> >> <animix_spam-this-ahole_@animas.net> wrote:
>>> >>
>>> >> >I've heard nothing but good things about the Silicon Image SATA
>>> >conrrollers.
>>> >> >I have a question about this though. If I'm reading my manual
>>> >>correctly,
>>> >>the
>>> >> >onboard VIA controller on my mobo has it's own separate bus for SATA
>>> >>RAID
>>> >> >which is independent of the PCI bus. I would think that, if this is
>>> >>the
>>> >> >case, using an onboard controller would be a better choice as long
>>> >>as
>>> >>>it's
>>> >> >got a separate bus. The Promise c
```

FfHdpe4v vZxoUYcjtqye
W+1SgQsFrOdxZIs1VtFWHI7LULmvtUghce7a1LMt3WsoqxX3KR5r7UDweC7s pY82R1roRxWI
SLNk2oCWC7tWWGfl6rQMTTWWhDNdJQwLMNnassjnyOqHjemhXmuujAtxWIEM+
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Xi0AXyVeR7oJyV74tEBVzZVXI3baWSVK63RGoRdJXTK7Wgm2VVst0IKubIUy 7nA2yvG1OgGi
RSKQkgdnoc
