
Subject: Blowout Sale!!! All must go!1 eds, 1 mec, 1 c-16, 2 IF2's.

Posted by [Joey](#) on Wed, 15 Nov 2006 16:00:02 GMT

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Subject: Re: Blowout Sale!!! All must go!1 eds, 1 mec, 1 c-16, 2 IF2's.

Posted by [harry epstein](#) on Thu, 16 Nov 2006 04:44:14 GMT

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AES output module-the only one in existence that I know =
of.
2 x=20

Paris 3.0 OS CD's and a bunch of earlier CD's
Brian T DVD

4 =
x EDS=20

1000 cards with interconnect cables
7 slot Magma chassis with host =
cards=20

and ribbon cable
3 x C-16's
8 x ADAT modules (one of them =
doesn't have a=20

faceplate or backplate but it
works fine)
3 x 24 bit input =
modules
3=20

x 24 bit output modules
4 x 15' SCSI 68 pin HD cables (brand =
new-had'em for=20

3 months)
3 x IF2's
1 AES output module-the only one in =
existence that I=20

know of.
2 x Paris 3.0 OS CD's and a bunch of earlier CD's
Brian =
T=20

DVD

4 x EDS 1000 cards with interconnect cables
7 slot Magma =
chassis

Subject: Re: Blowout Sale!!! All must go!1 eds, 1 mec, 1 c-16, 2 IF2's.

Posted by [Joey](#) on Thu, 16 Nov 2006 15:14:52 GMT

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Content-Type: text/plain;

charset="iso-8859-1"

Content-Transfer-Encoding: quoted-printable

Again, sorry.....e-mail addy is misspelled.=20

animix at animas dot net

"DJ" <www.aarrrrggghhh!!!.com> wrote in message news:45bd267b@linux...

I have decided to jump to an RME MADI system so everything must go:

Everything is blackface unless otherwise noted.

1 x MEC	\$125.00=20	
1 x MEC	\$125.00=20	
1 x MEC	\$125.00	
1 x MEC (If 2 port doesn't work)	\$100.00	=
	=20	

1 x EDS 1000 with interconnect cables	\$200.00=20
1 x EDS 1000 with interconnect cables	\$200.00=20
1 x EDS 1000 with interconnect cables	\$200.00
1 x EDS 1000	\$175.00
2 x Paris 3.0 CD's	\$100.00 (one was =

purchases independently, the other came with a card)=20

1 x C-16	\$ 75.00
1 x C-16	\$ 75.00
1 x C-16	\$ 75.00
1 x ADAT module	\$200.00

Subject: Re: Blowout Sale!!! All must go! 1 eds, 1 mec, 1 c-16, 2 IF2's.

Posted by [Dimitrios](#) on Wed, 22 Nov 2006 09:31:12 GMT

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<DIV>1 x ADAT=20

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<DIV>1 x ADAT=20

```
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module&nbsp;&nbsp;&nbsp;&nbsp;&nbsp;& ;&nbsp;&nbsp;&nbsp;&nbsp;&nbsp;&  
=
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n=

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