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Subject: Re: Follow up to Chris about the Intel patch
Posted by [Chris Ludwig](#) on Thu, 28 Jun 2007 14:16:32 GMT
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Subject: Re: Follow up to Chris about the Intel patch
Posted by [TCB](#) on Thu, 28 Jun 2007 15:19:40 GMT
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Subject: Re: Follow up to Chris about the Intel patch
Posted by [TCB](#) on Thu, 28 Jun 2007 17:31:05 GMT
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Subject: Re: Follow up to Chris about the Intel patch
Posted by [Chris Ludwig](#) on Thu, 28 Jun 2007 20:24:10 GMT
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not
=3D3D

be=3D3D2 0

 hooking =
up the=20
correct eds=3D20

 =

=20
card
to the master =
=3D

Subject: Re: Follow up to Chris about the Intel patch
Posted by [Neil](#) on Fri, 29 Jun 2007 00:46:49 GMT
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rd. Put=20
=3D3D

different=3D3D20

=20
=3D

modules in=20
the=3D20

 MECs =
so you can=20
see which one is on =3D3D3D

card A,=20
=3D

 =

=3D3D

B,=20
C=3D20

=20
at=3D3D20

 all times. this will give you the info =
as to how=20
=3D

=2 0

the=3D20

=20
=3D3D3D

cards =
=3D3D

are=3D3D20

=20
=3D

initializing and=20
which=3D20

 card =
is card=20
A, B, C as you start =
=3D3D

=3D3D3D

adding=20
=3D

=2 0

=20
more=3D3D20

 hardware. the MEC i sent you is one of =
4 that=20
=3D

=2 0

were=3D20

 =
working=20
=
=3D3D

=3D3D3D

perfectly=3D3D20

=20
=3D

when I took=20
my=3D20

 system =
off line.=20
I can't imagine it being =
a=3D20

=2 0

=20
=

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href=3D'http://www.polesoft.com/refer.html">

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