Subject: Follow up to Chris about the Intel patch
Posted by TCB on Thu, 28 Jun 2007 13:41:27 GMT
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sp; >=20
card</FONT></DIV><BR& gt;> >=20
=3D20
>   > =
<DIV><FONT=20
face=3D3D3D3DArial size=3D3D3D3D2>in your system as=20
=3D
> &a

Subject: Re: Follow up to Chris about the Intel patch
Posted by Chris Ludwig on Thu, 28 Jun 2007 14:16:32 GMT
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mp;nbsp;   >far=20
as=3D20
>   > =20
=3D3D
>Paris' ID =3D3D3D
>=20
=3D
 >   =2 0
>>orientation=3D3D3D20
&g t;=20
=3D20
>   > >=20
right?</FONT></DIV><BR&am p;gt;> >=20
=3D20
>   > =
<DIV><FONT=20
face=3D3D3D3DArial=20
=
size=3D3D3D3D2></FONT>=3D20
>   =
;=20
> </DIV>
> > =
<DIV>
>=20
=3D
>   >>=20
=3D20
>   > =
<DIV><FONT=20
face=3D3D3D3DArial size=3D3D3D3D2>Yes, it does, and=20
=3D
 >   =2 0
>the=3D20
>   > =20
=3D3D
>orientation of
> =
=3D3D3D
>=20
>the=3D20
>   > =20
slots=3D3D3D20
> > you are using in combination=20

Subject: Re: Follow up to Chris about the Intel patch
Posted by TCB on Thu, 28 Jun 2007 15:19:40 GMT
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t;face=3D3D3D3DArial=3D20
>   =
> =20
size=3D3D3D3D2>Start with a new SCSI cable =
=3D3D
>and a=20
=3D
>   =
>=3D3D3D
>=20
=3D20
>   > =
>single=20
EDS=3D3D3D20
> > card. Test each MEC.=20
=3D
>   >Once=20
you=3D20
>   > know =
if there is=20
a problem there, add
=3D3D
>a&lt ;BR>> =

=3D20
>   > =
=3D3D3D
>=20
>new SCSI=3D3D3D20
> > cable=20
=3D
>   >and=20
another=3D20
>   > =
card. Put=20
different modules in the MECs so you =3D3D
>can=20
=3D
>   =
>=3D3D3D
>=20
=3D20
>   > >see=20
which=3D3D3D20
> > one is on card A, B, C=20
=3D
>   >at=20
all=3D20
>   > times. =
this will=20
give you the info =3D3D
>=3D3D3D
&amp ;gt;=20
=3D
 >   =2 0
>>as=3D20
>   > =

to=3D3D3D20
> > how the cards are initializing =
and=20
=3D
> &a

Subject: Re: Follow up to Chris about the Intel patch
Posted by Graham Duncan on Thu, 28 Jun 2007 17:07:35 GMT

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3D
> =
=3D20
 >   =2 0
> =3D
 >   =2 0
=
>>cables.</FONT></DIV&amp ;gt;</DIV></BLOCKQUOTE>&l=
t;/B=3D
 >   =2 0
>ODY></HTML>
&a mp;gt;=20

=3D20
>   > =
>
>=20
>&l

Subject: Re: Follow up to Chris about the Intel patch
Posted by TCB on Thu, 28 Jun 2007 17:31:05 GMT
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;nbsp;   =2 0
>Polesoft=3D20
>   =
> =20
Lockspam to fight spam, =
and=3D20
 >   =2 0
> you?
>http://www.polesoft.com/refer.html=20
=3D20
>   > =20

>
>< !DOCTYPE HTML PUBLIC =
"-//W3C//DTD HTML=20
4.0=3D20
>   > =20
=
Transitional//EN" >
><HTML>&l t;HEAD>
>=
<META=3D20
 >   =2 0
> http-equiv=3D3D3DContent-Type=20
=
content=3D3D3D"text/html;=

Subject: Re: Follow up to Chris about the Intel patch
Posted by Chris Ludwig on Thu, 28 Jun 2007 20:24:10 GMT
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sp;=20
already.
I don't want to sound like a "RTFM" kind of=20
=3D
 >   =2 0
=
>=3D3D
>guy,=3D20
 >   =2 0
> but=3D3D20
> the Paris manual=20
describes
this =
=3D
 >   =2 0
>procedure pretty=3D20
>   =
> =20
well.
It's on =3D3D
>page =
4=3D3D20
> and 5=20
of =3D
>   >the=20
3.0=3D20
>   > =20
version.
It kind of sounds to me like you=20
may=3D20
>   > =20

not
=3D3D
>be=3D3D2 0
> hooking =
up the=20
correct eds=3D20
>   =
> =20
card
to the master =
=3D
 >   &nbs

Subject: Re: Follow up to Chris about the Intel patch
Posted by Neil on Fri, 29 Jun 2007 00:46:49 GMT
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rd. Put=20
=3D3D
>different=3D3D20<BR&am p;gt;>=20
=3D
>   >modules in=20
the=3D20
>   > MECs =
so you can=20
see which one is on =3D3D3D
>card A,=20
=3D
>   =
>=3D3D
>B,=20
C=3D20
>   > =20
at=3D3D20
> all times. this will give you the info =
as to how=20
=3D
 >   =2 0
>the=3D20
>   > =20
=3D3D3D
>cards =
=3D3D
>are=3D3D20
& amp;gt;=20
=3D
>   >initializing and=20
which=3D20
>   > card =
is card=20
A, B, C as you start =
=3D3D
>=3D3D3D
&amp ;gt;adding=20
=3D
 >   =2 0
>
>   > =20
more=3D3D20
> hardware. the MEC i sent you is one of =
4 that=20
=3D
 >   =2 0
>were=3D20
>   > =
working=20
=
=3D3D
>=3D3D3D
&amp ;gt;perfectly=3D3D20
>=20
=3D
>   >when I took=20
my=3D20
>   > system =
off line.=20
I can't imagine it being =
a=3D20
 >   =2 0
> =20
=

=3D3D
>=3D3D3D
&amp ;gt;problem.=3D3D20
> =
Bent=20
pins on =3D
>   >SCSI=20
cables=3D20
>   > can =
be a=20
serious issue though. =
Your=3D20
 >   =2 0
> =
=3D3D
>=3D3D3D&am

Subject: Re: Follow up to Chris about the Intel patch
Posted by Chris Ludwig on Fri, 29 Jun 2007 04:48:34 GMT
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sp; > =

>different=3D3D20
>   =

> > modules in the MECs so you can see =
which one=20
is on
>   =
=3D3D3D
>card=20
=3D
>   >A,=20
=3D3D
>   > =
>B, C=20
at=3D3D20
>   > =

> all times. this will give you the info as to =
how the=20
=3D
 >   =2 0
>=3D3D3D
>cards=20
=3D3D
>   > =20
>are=3D3D20
>   =
> =20
> initializing and which card is card A, B, C as =
you=20
start =3D3D
>   =
> =20
>=3D3D3D
>adding=20
more=3D3D20
>   =
> =20
> hardware. the MEC i sent you is one of 4 that =
were=20
working =3D3D
>   =
> =20
=
>=3D3D3D
>perfectly=3D3D20
>   =

 =20
> > when I took my system off line. I =
can't=20
imagine it being a =
=3D3D
 >   =2 0
> =20
=
>=3D3D3D
>problem.=3D3D20
>   &=
nbsp;=20
> > Bent pins on SCSI cables can be a =
serious=20
issue though. Your =
=3D3D
 >   =2 0
> =20
=
>=3D3D3D
>system=3D3D20
>   &nb=
sp;=20
> > will not function with bad SCSI=20
cables.
>
>& amp;lt;!DOCTYPE=20
=3D3D
>   > =20
>HTML=3D3D20
>   =
> =20
> PUBLIC "-//W3C//DTD HTML=20
4.0=3D3D20
>   =
> =20
> =20
=
Transitional//EN" >
><HTML>&l t;HEAD>
>=
<META=3D3D20
 >   =2 0
> > http-equiv=3D3D3D3DContent-Type=20
=
content=3D3D3D3D"text/html;=3D3D20
>   =

> > =20
=
=3D3D3D
>charset=3D3D3D3Diso-8859-1">
><MET=
A=20
=3D3D
>   > =20
=
>content=3D3D3D3D"MSHTML=3D3D

Subject: Re: Follow up to Chris about the Intel patch
Posted by John [1] on Fri, 29 Jun 2007 11:33:39 GMT
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0
&g

Subject: Re: Follow up to Chris about the Intel patch
Posted by Neil on Fri, 29 Jun 2007 17:48:20 GMT
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nbsp;=2 0
> =3D
 >   =2 0
>>href=3D3D3D"<A=20
=
href=3D'http://www.polesoft.com/refer.html">

Subject: Re: Follow up to Chris about the Intel patch
Posted by Chris Ludwig on Fri, 29 Jun 2007 18:58:52 GMT
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ng</FONT></DIV>
>   =

><DIV><FONT face=3D3DArial=20
=
size=3D3D2>drivers.</FONT></DIV& amp;gt;
>   =
  =20
><DIV><FONT face=3D3DArial =
size=3D3D2></FONT>=20
</DIV>
 >   =2 0
><DIV><FONT face=3D3DArial size=3D3D2>I am =
only able to=20
intermitantly
>   get=20
the
>   =20